

## MBRF2020CT THRU MBRF20100CT

### SCHOTTKY BARRIER RECTIFIER

Reverse Voltage - 20 to 100 Volts Forward Current - 20.0 Ampere

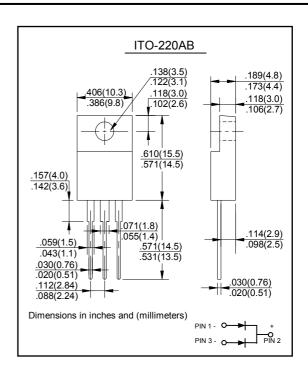
#### **FEATURES**

- Plastic package has Underwriters Laboratory Flammability Classification 94V-0
- Dual rectifier construction, positive center tap
- Metal silicon junction, majority carrier conduction
- Low power loss, high efficiency
- Guardring for overvoltage protection
- For use in low voltage, high frequency inverters, free wheeling, and polarity protection applications

#### **MECHANICAL DATA**

- Case: JEDEC ITO-220AB molded plastic body
- Terminals: Plated leads, solderable per MIL-STD-750, Method 2026
- High temperature soldering guaranteed: 250°C/10 seconds, 0.25" (6.35mm) from case
- Polarity: As marked
- Mounting Position: Any
- Mounting Torque: 10 in-lbs maximum
- Weight: 0.08 ounce, 2.24 grams





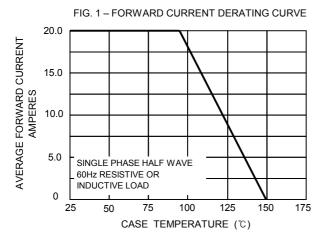
#### **MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS**

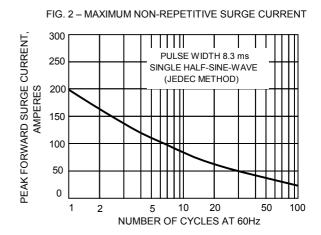
Ratings at 25°C ambient temperature unless otherwise specified. Single phase half-wave 60Hz, resistive or inductive load, for capacitive load current derate by 20%.

Characteristic	Symbol	MBRF 2020CT	MBRF 2030CT	MBRF 2040CT	MBRF 2045CT	MBRF 2050CT	MBRF 2060CT	MBRF 2080CT	MBRF 20100CT	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	VRRM VRWM VR	20	30	40	45	50	60	80	100	V
RMS Reverse Voltage	VR(RMS)	14	21	28	32	35	42	56	70	V
Average Rectified Output Current @T <sub>C</sub> = 95°C	lo	20								Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	İFSM	200								Α
Forward Voltage @I <sub>F</sub> = 10A	VFM	0.55 0.75 0.85					85	V		
Peak Reverse Current @T <sub>A</sub> = 25°C At Rated DC Blocking Voltage @T <sub>A</sub> = 100°C	lгм	0.5 100								mA
Typical Junction Capacitance (Note 1)	Cj	1100								pF
Operating and Storage Temperature Range	Тj, Tsтg	-65 to +150								°C

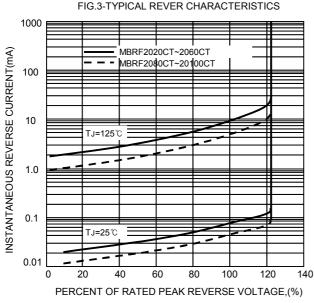


# MBRF2020CT THRU MBRF20100CT RATINGS AND CHARACTERISTIC CURVES











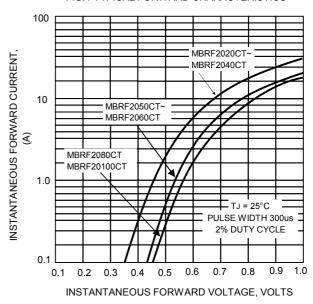


FIG.5 - TYPICAL JUNCTION CAPACITANCE

